PRAHLADRAI DALMIA LIONS COLLEGE OF COMMERCE & 

ECONOMICS ISO 9001: 2015 Certified

NOTICE

**6 October , 2025**

ATKT Internal and Practical Examination

B.Sc.I.T. (SEMESTER-I)

INSTRUCTIONS FOR THE STUDENTS HAVING ATKT IN INTERNALS

1. Date of Submission of the Assignments- **8th October , 2025**

2. Timings 11:00 AM to 12:00 Noon. Reporting time for students: at least 10 minutes before the mentioned time. Venue: Computer Lab.

3. Students have to be present in person for the submission.

4. Submission of assignments to be done on proper A4 size paper, handwritten by the candidate himself only. The Front page should contain details of Roll no, Name of the student, Semester, Subject.

5. Print out of the questions uploaded should be attached along with the assignment. Students should enclose a photocopy of the ATKT fee paid receipt along with each of his projects.

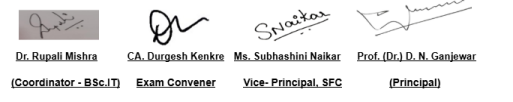
6. On the date of submission there will be a viva voce on the given questions/topics.

7. If the student fails to present himself on the given date and time he will be marked ABSENT for the said subject.

8. Any Submissions after the above mentioned date and time will not be accepted and entertained under any circumstances.

9. The students having ATKT in external practicals, have to Perform a given Practical Question.

**NOTE** - Student who has paid ATKT fees for internal components but has not been allotted questions or has any query is requested to contact Dr. Rupali Mishra on or before, 7 October ,2025 by mailing on bscit@dalmialionscollege.ac.in



| DI/N-STD/GEN/00 |
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**Internals**

**Subject : Combinational and Sequential Design**

| **Name Of**  **The**  **Student** | **Subject Name And Questions** |
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| 132  PRAJAPATI SATISHKUMAR NANHELAL | 1. Implement AND, OR, NOT, XOR, XNOR using NAND gates. 2. Implement XOR, XNOR using NOR gates. 3. Verify De Morgan’s laws. 4. Design and implement Full adder. 5. Design and implement 4:1 multiplexer. |